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# KENDALL L. LOWREY

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<b>OBJECTIVE</b>	To apply my knowledge and creativity to solve interesting engineering problems.		
<b>EDUCATION</b>	<b>CARNEGIE MELLON UNIVERSITY</b> <i>Pittsburgh, PA</i>		
	<ul style="list-style-type: none"><li>Bachelor of Science in Electrical and Computer Engineering</li><li>Additional Major in Biomedical Engineering</li><li>Minor in Computer Science</li></ul>		May 2010 May 2010 May 2010
<b>WORK EXPERIENCE</b>	<b>DEEPLocal, INC. HARDWARE ENGINEER</b> <i>Pittsburgh, PA</i>		Summer 2010
	<ul style="list-style-type: none"><li>Worked with designers to engineer advertiser's campaigns at this innovation studio.</li></ul>		
	<b>SPACE X: AVIONICS INTERN</b> <i>Hawthorne, CA</i>		Summer 2009
	<ul style="list-style-type: none"><li>Real-time flight computer operating system testing and improvement, with development to minimize task launch latencies for flight software.</li><li>Development of network testing software suite to measure performance and stress tests a distributed embedded network.</li></ul>		
	<b>CISCO SYSTEMS: SOFTWARE INTERN</b> <i>San Jose, CA</i>		Summer 2008
	<ul style="list-style-type: none"><li>Worked within Cisco's NSSTG group and a remote team based in Canada.</li><li>Developed websites and web-based widgets to display crucial information to Cisco managers in efficient and innovative ways.</li></ul>		
<b>PROJECTS</b>	<b>SIGN LANGUAGE VOCALIZATION DEVICE</b> <i>Biomedical Design</i>		Spring 2010
	<ul style="list-style-type: none"><li>Lead technical design and implementation of wrist-worn embedded device to convert gestured American Sign Language and the ASL alphabet to spoken words.</li></ul>		
	<b>AUTONOMOUS HELICOPTER CONTROL SYSTEM</b> <i>Embedded Design</i>		Spring 2009
	<ul style="list-style-type: none"><li>Lead a 4-person team with the goal of utilizing a 6DOF IMU and a dsPIC30F microcontroller to create a stability control system for a small collective pitch helicopter.</li></ul>		
	<b>Dual-Core Multi-Threaded Processor Design</b> <i>Computer Architecture</i>		Spring 2009
	<ul style="list-style-type: none"><li>In a two person group, designed and implemented a 5-stage pipelined processor with multi-threading on a dual core configuration, with caching and memory access for MIPS code.</li></ul>		
	<b>REAL-TIME KERNEL ON ARM CPU</b> <i>Embedded Real Time Systems</i>		Fall 2008
	<ul style="list-style-type: none"><li>Programmed a kernel in C and Arm ASM for an ARM7 processor to allow for rate-monotonic scheduling of periodic tasks.</li><li>Implemented mutexes and software/hardware interrupts for handling tasks and user programs operating in an environment with resource contention.</li></ul>		
<b>SKILLS</b>	<b>Programming Languages:</b>	C/C++, Arm ASM, x86 ASM, Java, Matlab, Verilog	
	<b>Languages:</b>	English, Chinese, 4 yrs High School Japanese Study	
	<b>Operating Systems:</b>	Windows, MacOSX, Linux/Unix Environments	
	<b>Software:</b>	Microsoft Office, Eclipse, OpenCV, Matlab, Subversion	
	<b>U.S. Citizen</b>		