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KENDALL L. LOWREY

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OBJECTIVE	To apply my knowledge and creativity to solve interesting engineering problems.		
EDUCATION	CARNEGIE MELLON UNIVERSITY <i>Pittsburgh, PA</i>		
	<ul style="list-style-type: none">Bachelor of Science in Electrical and Computer EngineeringAdditional Major in Biomedical EngineeringMinor in Computer Science		May 2010 May 2010 May 2010
WORK EXPERIENCE	DEELOCAL, INC. HARDWARE ENGINEER <i>Pittsburgh, PA</i> Summer 2010		
	<ul style="list-style-type: none">Worked with designers to engineer advertiser's campaigns at this innovation studio.		
	SPACE X: AVIONICS INTERN <i>Hawthorne, CA</i> Summer 2009		
	<ul style="list-style-type: none">Real-time flight computer operating system testing and improvement, with development to minimize task launch latencies for flight software.Development of network testing software suite to measure performance and stress tests a distributed embedded network.		
	CISCO SYSTEMS: SOFTWARE INTERN <i>San Jose, CA</i> Summer 2008		
	<ul style="list-style-type: none">Worked within Cisco's NSSTG group and a remote team based in Canada.Developed websites and web-based widgets to display crucial information to Cisco managers in efficient and innovative ways.		
PROJECTS	SIGN LANGUAGE VOCALIZATION DEVICE <i>Biomedical Design</i> Spring 2010		
	<ul style="list-style-type: none">Lead technical design and implementation of wrist-worn embedded device to convert gestured American Sign Language and the ASL alphabet to spoken words.		
	AUTONOMOUS HELICOPTER CONTROL SYSTEM <i>Embedded Design</i> Spring 2009		
	<ul style="list-style-type: none">Lead a 4-person team with the goal of utilizing a 6DOF IMU and a dsPIC30F microcontroller to create a stability control system for a small collective pitch helicopter.		
	Dual-Core Multi-Threaded Processor Design <i>Computer Architecture</i> Spring 2009		
	<ul style="list-style-type: none">In a two person group, designed and implemented a 5-stage pipelined processor with multi-threading on a dual core configuration, with caching and memory access for MIPS code.		
	REAL-TIME KERNEL ON ARM CPU <i>Embedded Real Time Systems</i> Fall 2008		
	<ul style="list-style-type: none">Programmed a kernel in C and Arm ASM for an ARM7 processor to allow for rate-monotonic scheduling of periodic tasks.Implemented mutexes and software/hardware interrupts for handling tasks and user programs operating in an environment with resource contention.		
SKILLS	Programming Languages: Languages: Operating Systems: Software: U.S. Citizen	C/C++, Arm ASM, x86 ASM, Java, Matlab, Verilog English, Chinese, 4 yrs High School Japanese Study Windows, MacOSX, Linux/Unix Environments Microsoft Office, Eclipse, OpenCV, Matlab, Subversion	